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Errata

Page 67, Table 2, Line 4, should read: &MN L 0, PAR 1 (1)

Pages 113 and 114, the fifth statement of each appendix should read: 2B00 SDR E1, E1 RETURN WITH NORMAL ZERO

Page 174, Footnote 2 should read: If x memory cycles are required ..., then n units of time will be required ...

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